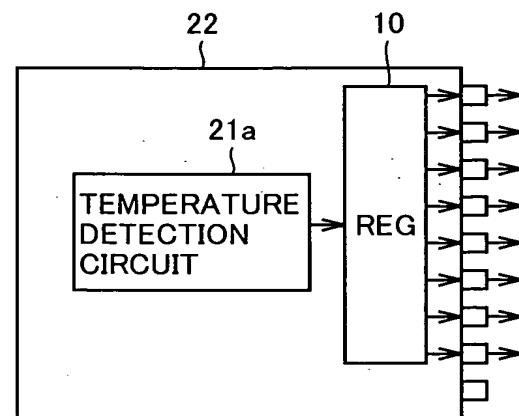


FIG.1



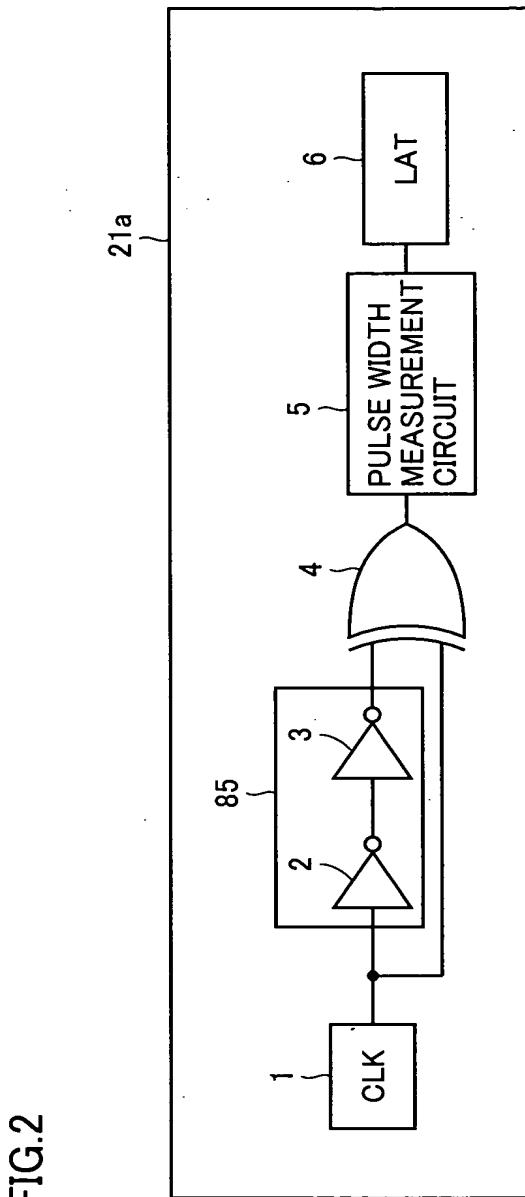


FIG.3

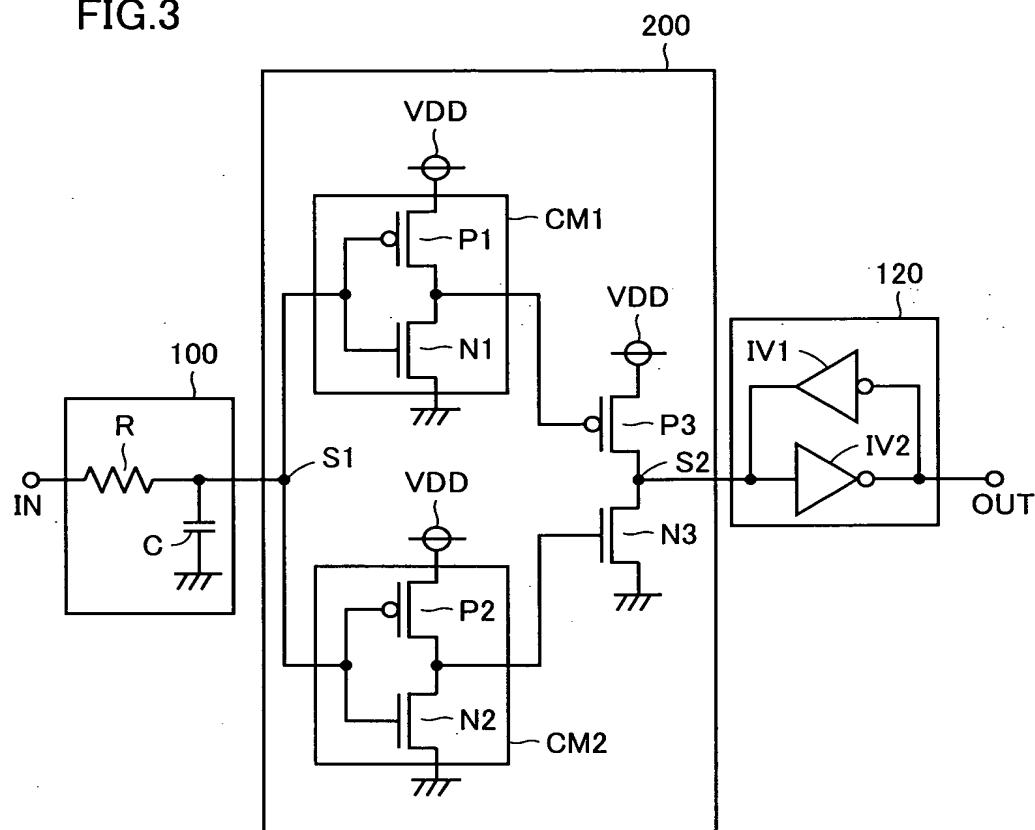


FIG.4A

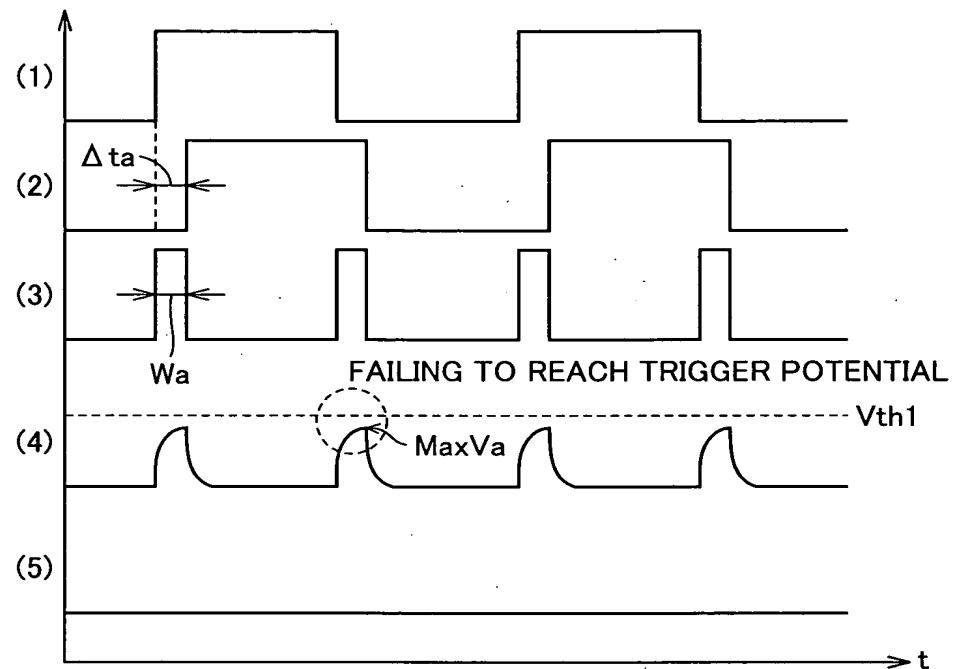
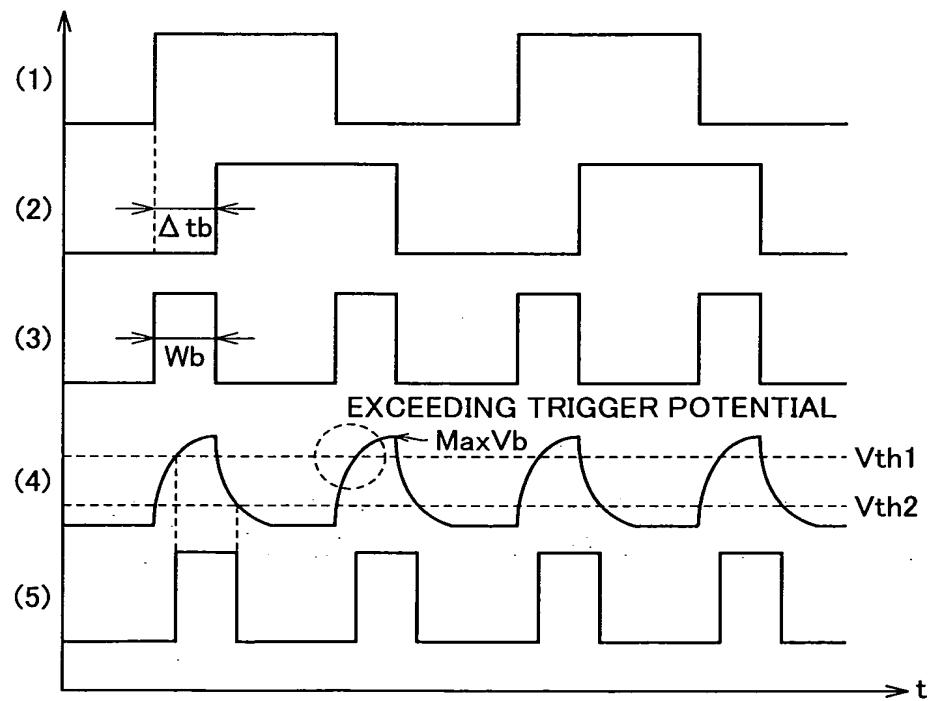


FIG.4B



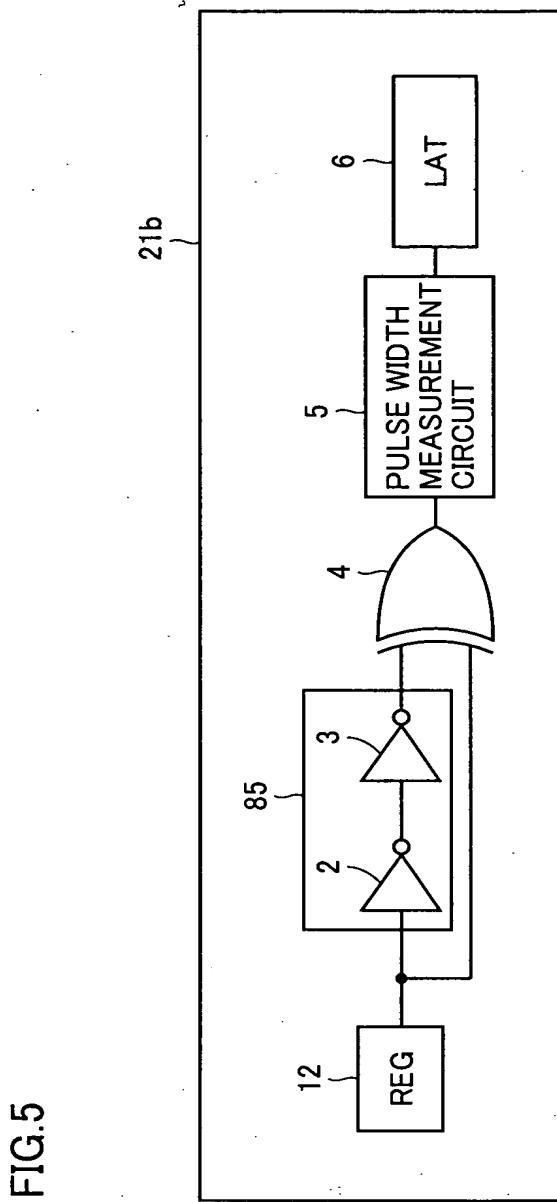


FIG.5

FIG.6

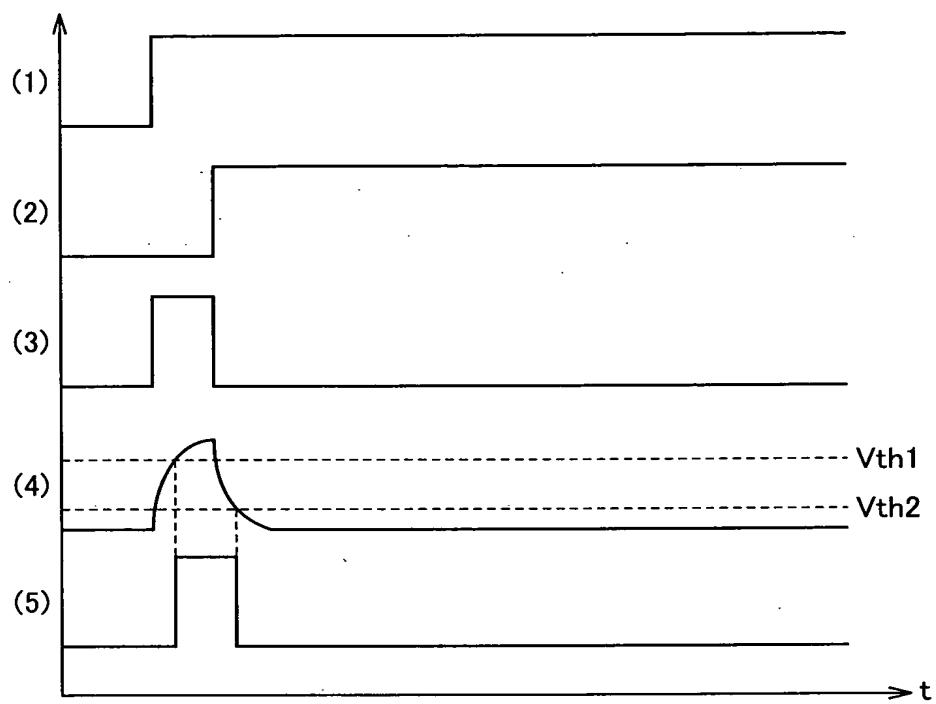
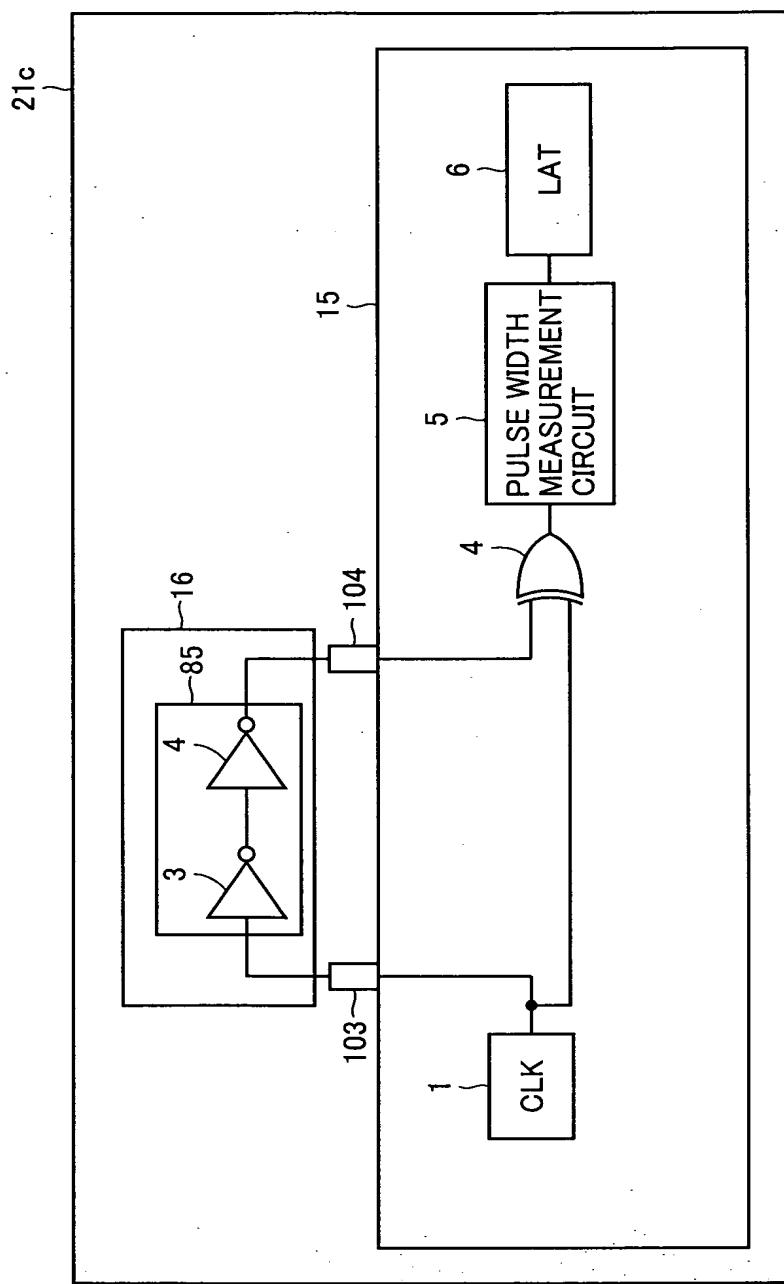
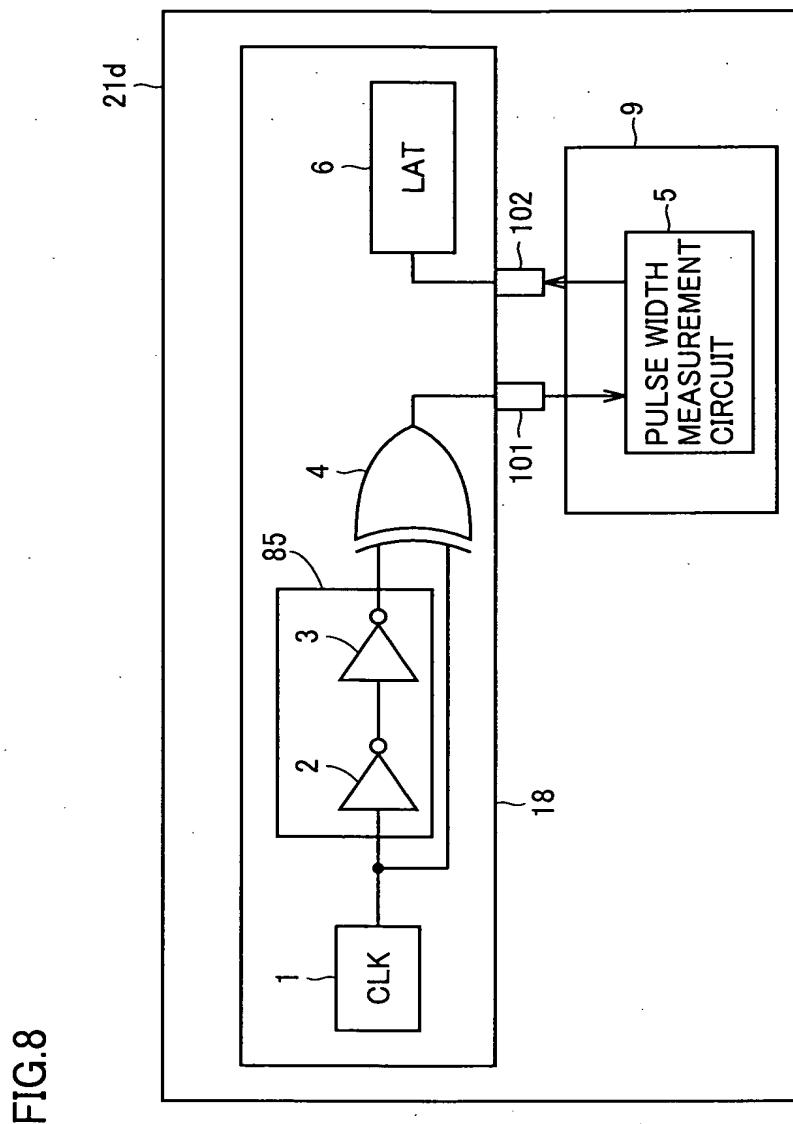


FIG.7





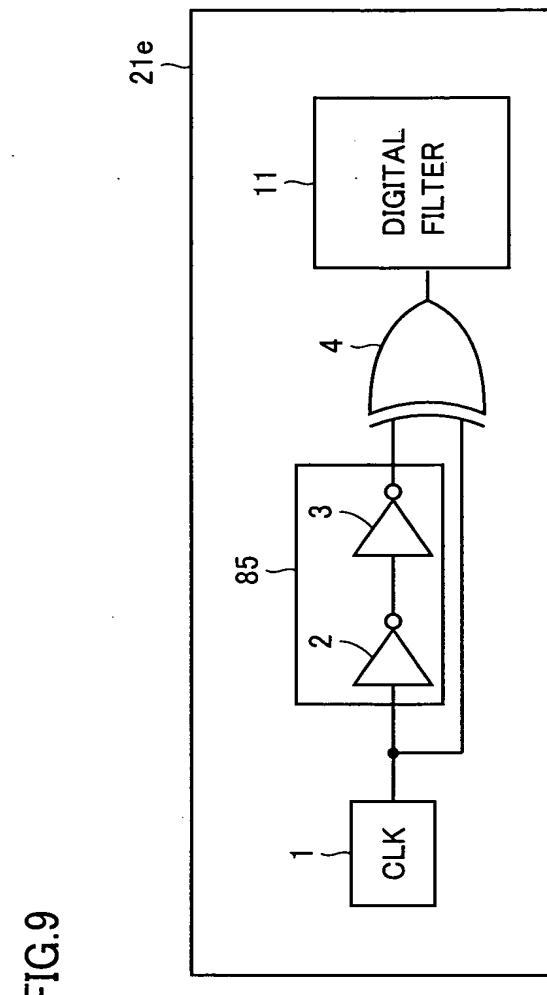


FIG.9

FIG.10

21f

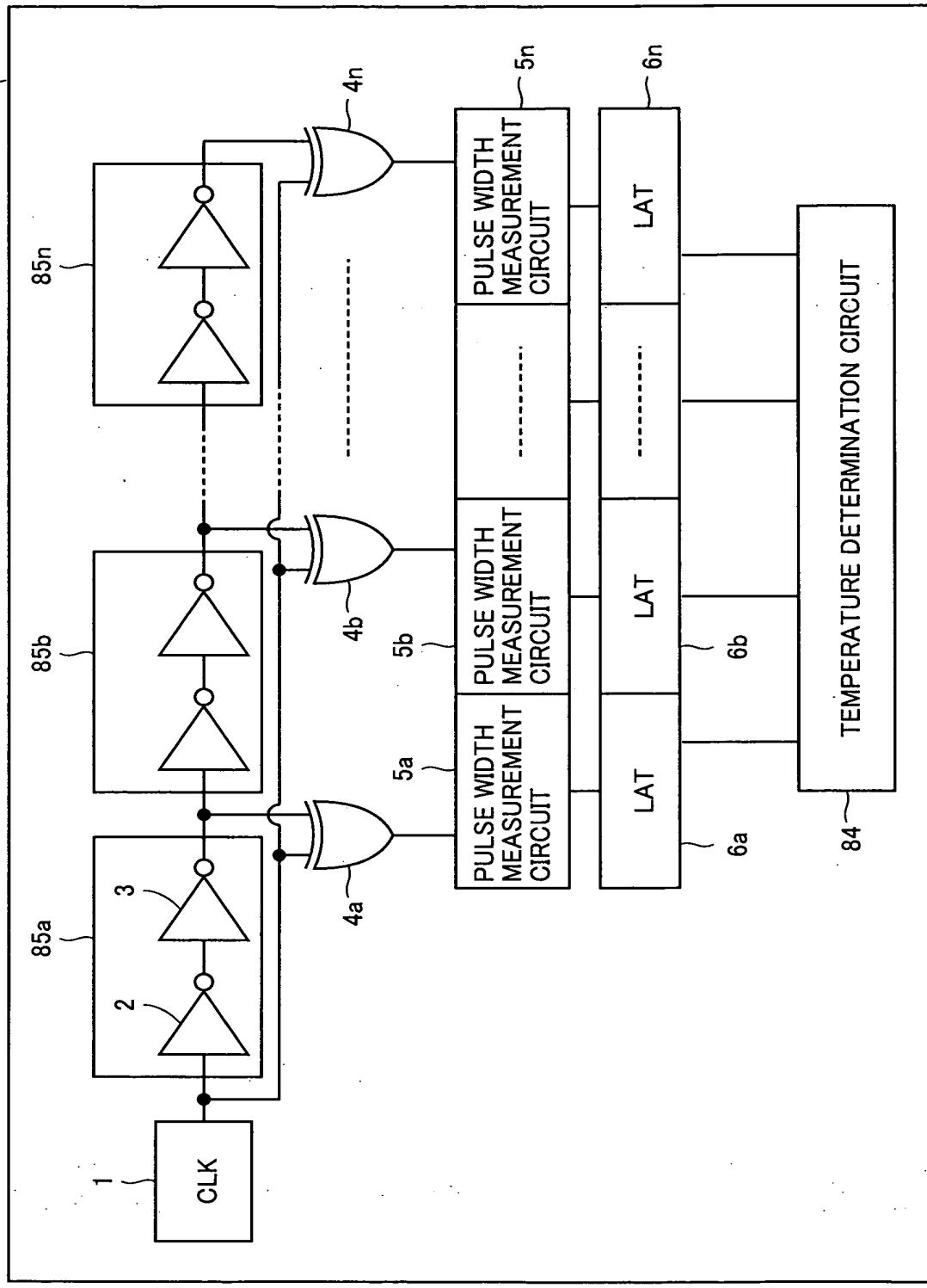


FIG.11A

LATCH CIRCUIT	VALUE OF SIGNAL HELD
LATCH CIRCUIT6a	L
LATCH CIRCUIT6b	L
LATCH CIRCUIT6c	L
LATCH CIRCUIT6d	H
.	.
.	.
LATCH CIRCUIT6m	H
LATCH CIRCUIT6n	H

FIG.11B

LATCH CIRCUIT	VALUE OF SIGNAL HELD
LATCH CIRCUIT6a	L
LATCH CIRCUIT6b	H
LATCH CIRCUIT6c	H
LATCH CIRCUIT6d	H
.	.
.	.
LATCH CIRCUIT6m	H
LATCH CIRCUIT6n	H

FIG.12

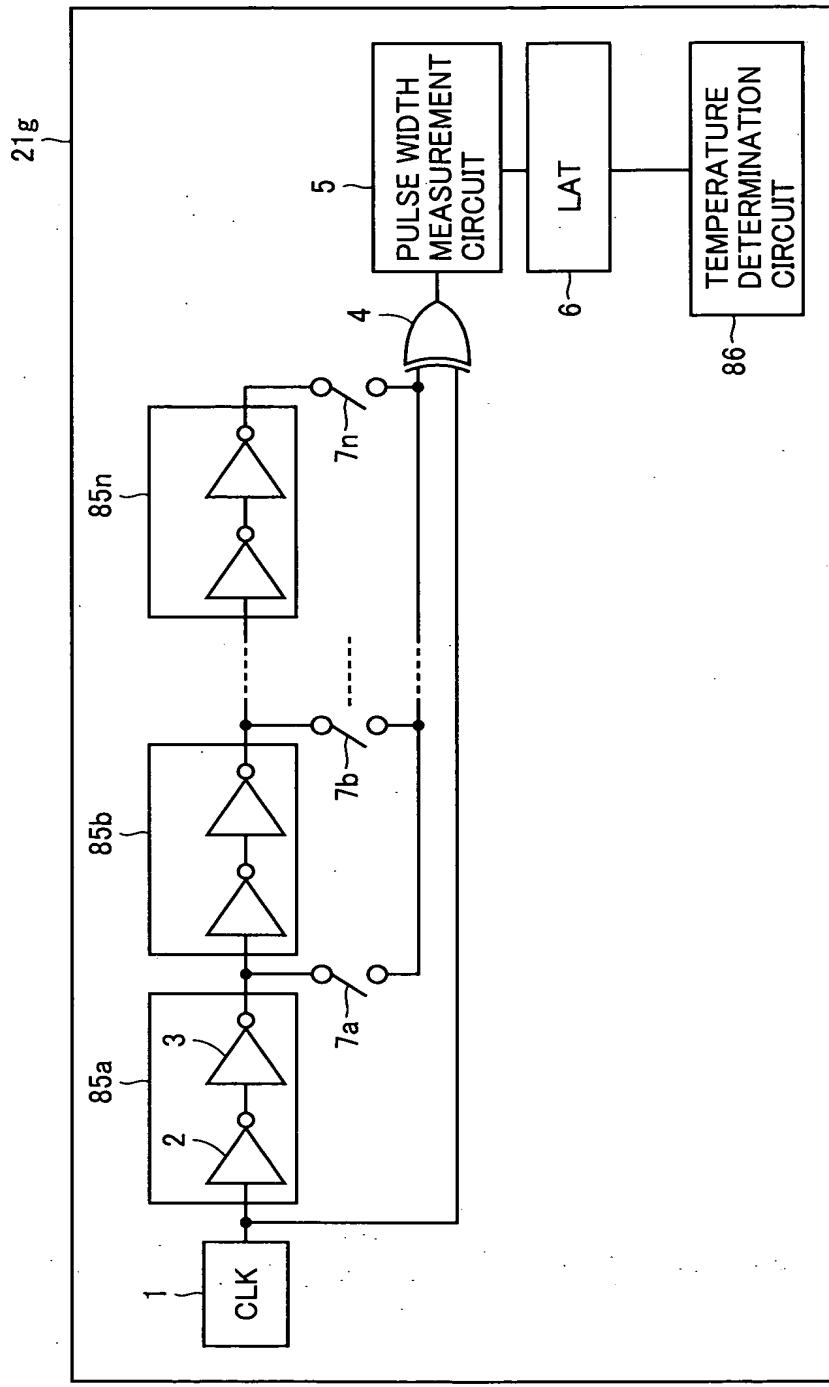


FIG.13

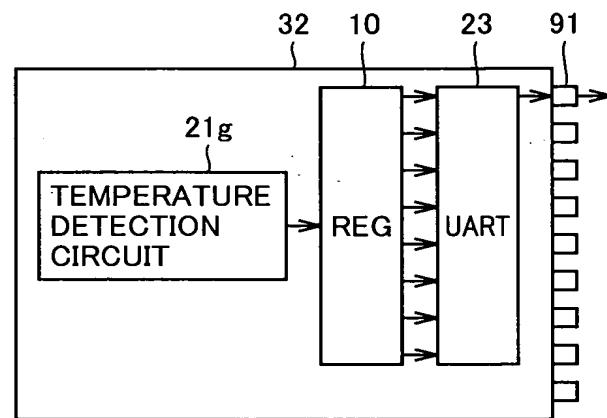


FIG.14

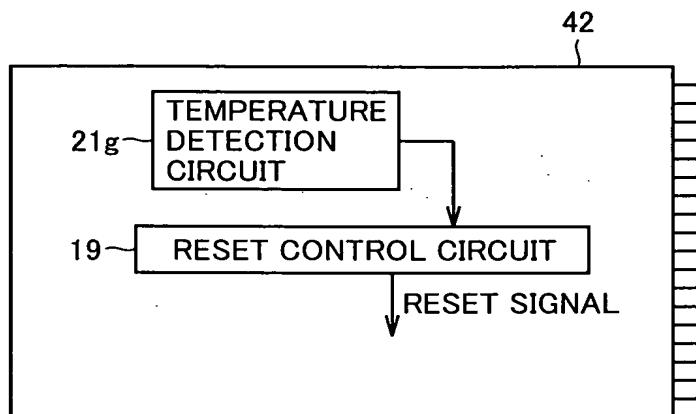


FIG.15

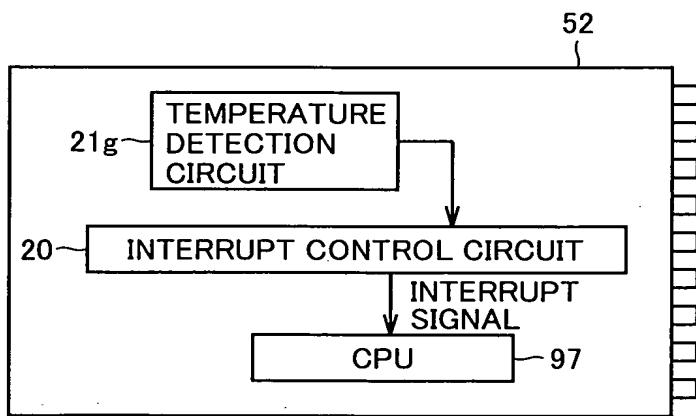


FIG.16

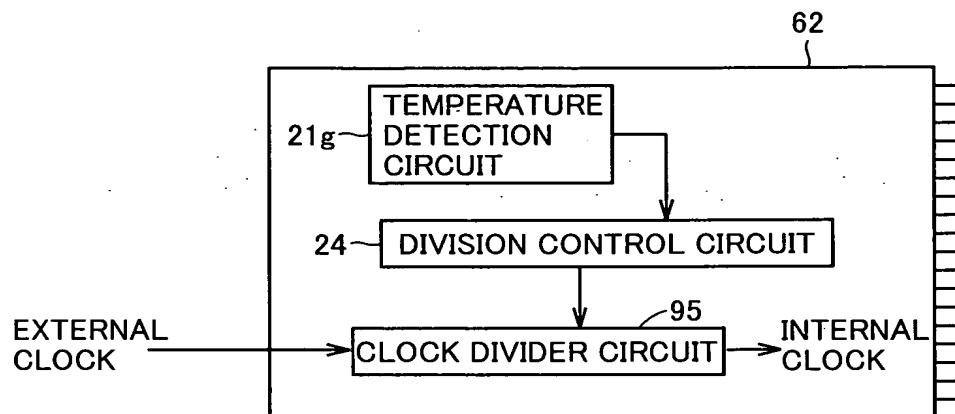


FIG.17

